



## **ELECTRICAL ENGINEER**

Quasar Federal Systems, Inc. seeks a proven engineer to design, develop, test, and maintain electronic hardware components, systems, and embedded controller firmware, for a variety of electromagnetic sensor products. QFS is a leading developer of high performance electromagnetic sensors systems, providing advanced sensors and data acquisition systems for a diverse range of commercial and defense applications.

### **POSITION DESCRIPTION**

- Design & test electromagnetic sensor systems and components, including embedded firmware (VHDL & C++) for sophisticated data acquisition systems.
- Prepare PCB design packages, including schematics, component specs, layout, power distribution, and I/O interfaces.
- Conduct design verification & validation and ensure regulatory compliance throughout development process.
- Develop product documentation, including system requirements, functional specs, validation & verification plans, assembly & test procedures, and user manuals.
- Transition final designs to production.
- Provide post-release support to customers including on-site support.
- Evaluate, troubleshoot, and redesign current products to improve performance, meet changing system requirements or address obsolescence issues.
- Function effectively in a cross-discipline team environment. Provide project leadership and mentor others.
- Self-starter, proficient at organizing and prioritizing tasks in fast-paced environment

### **POSITION QUALIFICATIONS**

- BS in Electrical Engineering
- >2 years experience developing hardware and embedded systems for complex integrated systems.
- Experience in >3 of the following
  - Expertise in analog & digital circuit design: digital core, interfacing sensors, I/O circuits, power supplies, and user interfaces
  - Experience in project management or supervisory role; ability to organize and communicate complex technical information to non-engineering personnel; strong technical writing skills
  - Analog circuit design and analysis
  - Digital circuit design and verification (using VHDL)
  - Firmware developer using C/C++ compilers
  - RF circuit design and characterization
- Experience in project management or supervisory role; ability to organize and communicate complex technical information to non-engineering personnel; strong technical writing skills
- Schematic capture and PCB layout
- Documentation



- Effective lab techniques: expertise in RF instrumentation (network and spectrum analyzers), calibration techniques, PCB re-work, and low-level prototyping
- Windows application software: C++, LabView, Excel
- FPGA design, development, simulation and test
- PADS (or comparable EDA tool), PSpice or other circuit simulation SW
- System architecture and product feature-set planning including: performance analysis, estimating BOM cost
- Digital down conversion and filtering

Salary: Commensurate with experience

Please send resumes to [jobs@quasarfs.com](mailto:jobs@quasarfs.com)